

SNJB's S.H.H.J.B. Polytechnic, Chandwad.

Department: Electronic And Telecommunication Engineering

Faculty Profile



Prof. Paithankar Prasad R.

Qualification :	ME (Embedded & VLSI), BE E&TC, DE E&TC
Designation :	Lecturer
Date of Joining :	7/7/2021
Experience :	4 Year
Teaching/research Areas :	Microcontroller, Microprocessor, Power Electronics, Embedded & VLS, Basic Electronics
Courses / Subjects Involved With :	ME3I-Basic Electrical and Electronics (BEE-22310) CO2I-Elements of Electrical Engineering (EEC-22215) EJ4I –Microcontroller and Application (MAA-22426) EE3I- Power Electronics EE4I-Digital Technique & Microcontroller. CO4I-Microprocessor
Staff training/FDP attended in last 3 years	1)Webinar on Digital Twin Technology at ICOER, Wagholi Pune. 2) Webinar on Design, Development & Implementation of Project Methodologies at AIISMS College of Engg. Pune. 3)One week online faculty Development program on Higher Education 2020: Requirements & Expectations.
Paper presentation /conferences attended	Presented Technical paper on Arduino Based vehicle tracking & Women Tracking System in ePGPEX 2020.
Paper published in journals	1)“ Automatic Safe Landing-Site Detection System ” in International Engineering Research Journal (IERJ) Volume 2 Issue 1 Page 154-156, 2016, ISSN 2395-1621 dated on 12th March 2016. 2)“ Arduino Based Women Safety Security System ” International Journal for Research in Engineering Application & Management (IJREAM) ISSN : 2454-9150 Vol-06, Issue-04, July 2020
Books published with ISSN/ISBN	NIL

number :	
Participation in MSBTE activities (RAC/DC/Theory/PR Exam/Curriculum development)	1) MSBTE Exam Controller 2)Worked as Officer-in-charge, 3)Internal squad member & - External Vigilance squad member. 4)Worked as Examiner. - Online Exam In charge 5)PR- Internal/ External examiner
List of Projects guided at diploma level last 3 years	1) Arduino Based vehicle tracking & Women Tracking System 2) Arduino Based Automatic bottle filling Plant.
Role and responsibilities at institute level	
Role and responsibilities at department level	DSY Admission Co-ordinator Time Table Incharge Communication Lab Incharge
Major achievements in last 5 years	NIL
Professional membership if any	NIL
Additional information	NIL